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COWAN LIEBOWITZ & LATMAN

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MAY 25 2005

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Eugene L. Flanagan III (Depositor's name)
Eugene L. Flanagan III (Signature)
May 25, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/748,580	12/30/2003	Tsin-Yuan Chang	03901-P0004A	8240

TITLE OF INVENTION: METHOD AND APPARATUS FOR CONTROLLING A DUAL-SLOPE INTEGRATOR CIRCUIT TO ELIMINATE SETTLING TIME EFFECT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$1000	05/25/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
WELLS, KENNETH B	2816		327-344000		

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National Tsing Hua University

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Eugene L. Flanagan III

Date May 25, 2005

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Fax Cover Sheet

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Re: Application No. 10/748,580

Filing Date: December 30, 2003

Applicant: Tsin-Yuan Chang et al

**For: METHOD AND APPARATUS FOR CONTROLLING A DUAL-SLOPE
INTEGRATOR CIRCUIT TO ELIMINATE SETTLING TIME EFFECT**

Our Ref: 25904.004

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